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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10045416	10/26/2001	327	713	2816	LE

**APPLICANTS: Kunst David; Vinn Charles;

not ASD

**CONTINUING DATA VERIFIED: None ASD 10/27/03

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** FOREIGN APPLICATIONS VERIFIED: None ASD 10/27/03

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		A-70429/ENB
Verified and Acknowledged Examiners's initials ASD		
TITLE : Method and apparatus for optimizing the accuracy of an electronic circuit		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.
			Print Fig.
TERMINAL DISCLAIMER		Primary Examiner	
		PREPARED FOR ISSUE	
		Application Examiner	
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